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11/07/01

U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10014246	11/07/2001	21244	626	3637	Keenan

****APPLICANTS:** Huber Ronald; Klabsch Rolf-Arno;

****CONTINUING DATA VERIFIED:** YES

THIS APPLICATION IS A CON OF PCT/DE00/01453 05/05/2000

**** FOREIGN APPLICATIONS VERIFIED:** YES

GERMANY 199 21 246.5 05/07/1999

PG-PUB ☐ DO NOT PUBLISH ☐

RESCIND ☐

Foreign priority claimed

☒ yes ☐ no

35 USC 119 conditions met

☒ yes ☐ no

Verified and Acknowledged Examiners's initials

JR 4/7/03

ATTORNEY DOCKET NO

1999P8051

TITLE : Installation for fabricating semiconductor products

U.S. DEPT. OF COMM /PAT. & TM-PTO-436L(Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
			Total Claims	Print Claim for O.G.
ISSUE FEE		Primary Examiner	DRAWING	
Amount Due	Date Paid		Sheets Drwg.	Figs. Drwg.
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	Application Examiner	
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